

FEATURES

- HIGH RELIABILITY FOR LOW COST
- TEMPERATURE STABILITY OF +/-1 PPM AVAILABLE
- EXCELLENT PHASE NOISE PERFORMANCE
- LOW POWER CONSUMPTION
- CHEAPEST AVAILABLE LEADED SMD-TCXO IN METAL PACKAGE
- EXTENDED TEMPERATURE RANGE TO -40/+85°C AVAILABLE

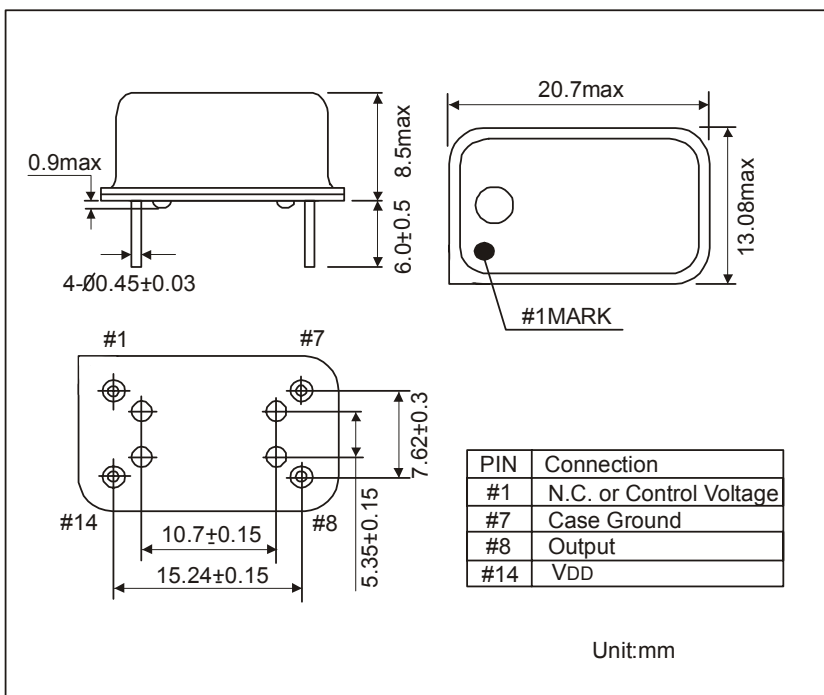
SERIES		M4311
PACKAGE		SMD-PACKAGE 11.4 x 9.6 x 2.5 mm ³
FREQUENCY RANGE	CLIPPED SINE WAVE	10.0 ~ 35.0 MHz
FREQUENCY STABILITY	VS. TEMPERATURE	+1 ~ +5 ppm
	VS. SUPPLY VOLTAGE	+0.5 ppm max. / 3.3 VDC +5%
	VS. LOAD	+0.2 ppm max. / 10 pF ~ 20 pF
	VS. AGING	+1 ppm max. per year
OPERATING TEMPERATURE RANGE		-0/+50°C ~ -40/+85°C
STORAGE TEMPERATURE RANGE		-40/+85°C
INPUT	VOLTAGE	+3.3 VDC +5%
	CURRENT	2 mA max.
OUTPUT	SIGNAL	CLIPPED SINE WAVE
	VOLTAGE	0.7 V peak to peak min.
	LOAD	10 kΩ / 10 pF
FREQUENCY ADJUSTMENT	STANDARD	+3 ppm min. per internal trimmer
	OPTION	+5 ppm min. by external control voltage
EXTERNAL CONTROL VOLTAGE		1.65 VDC +/-1.35 VDC
PHASE NOISE (typical)	10 Hz offset from carrier	-80 dBc/Hz
	100 Hz offset from carrier	-105 dBc/Hz
	1 kHz offset from carrier	-140 dBc/Hz
	10 kHz offset from carrier	-150 dBc/Hz
	100 kHz offset from carrier	-155 dBc/Hz
PIN CONNECTION	PIN 1	NOT CONNECTED OR CONTROL VOLTAGE
	PIN 2	CASE GROUND
	PIN 3	OUTPUT
	PIN 4	SUPPLY VOLTAGE

OTHER PARAMETERS ARE AVAILABLE ON REQUEST / CREATE HERE YOUR SPECIFICATION

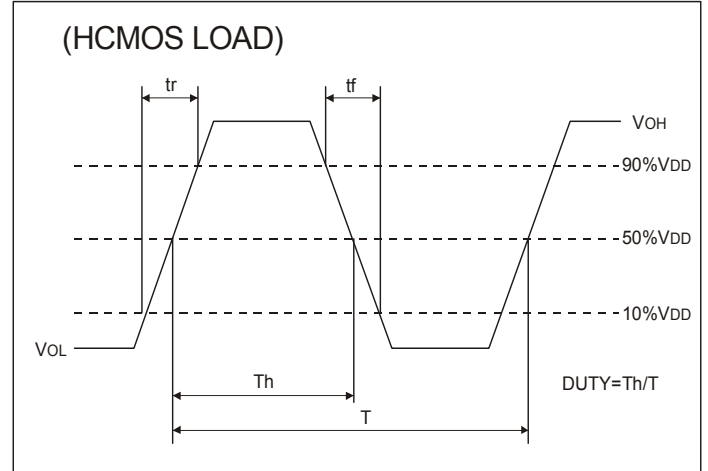
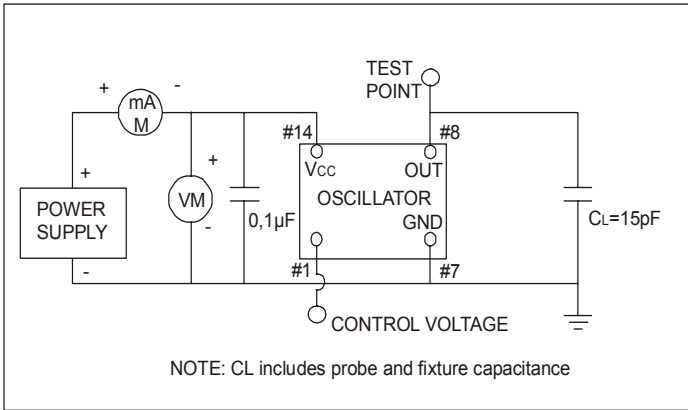
PART NUMBERING GUIDE

EXAMPLE	M4300-S-J-10-S-V-27.000MHz
SERIES	M4300
OUTPUT SIGNAL	S = CLIPPED SINE WAVE
	T = TTL
	H = HCMOS
TEMPERATURE RANGE	J = 0/+50°C
	N = -10/+60°C
	X = 0/+70°C
	M = -20/+70°C
	K = -30/+75°C
	W = -40/+85°C
STABILITY VS. TEMPERATURE	10 = +/-1.0 ppm
	15 = +/-1.5 ppm
	20 = +/-2.0 ppm
	25 = +/-2.5 ppm
	50 = +/-5.0 ppm
	X = OTHER VALUE - PLEASE INDICATE YOUR REQUIRED VALUE
SYMMETRY FOR HCMOS / TTL	BLANK = 40/60%
	S = 45/55%
VCTCXO OPTION	V = 1.65 +/-1.35 VDC for +/-5 ppm min.
	X = OTHER VALUE - PLEASE INDICATE YOUR REQUIRED VALUE
FREQUENCY	FREQUENCY IN MHz

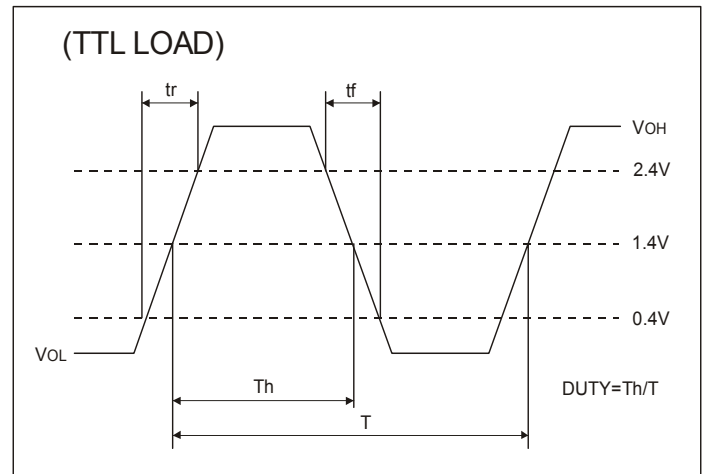
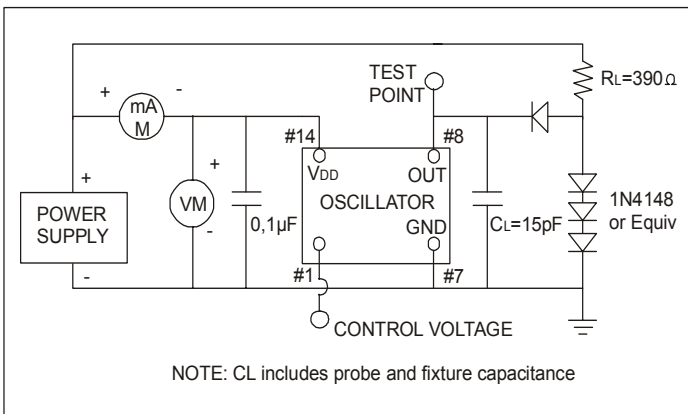
OUTLINE DRAWING M4300



TEST CIRCUIT FOR HCMOS



TEST CIRCUIT FOR TTL



TEST CIRCUIT FOR CLIPPED SINE WAVE

